

# Practical Low Power Digital VLSI Design

By Gary K. Yeap



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and Discharging Capacitance --1.3 Short

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Low-Power Digital VLSI Design: Power dissipation in CMOS circuits, several practical circuit examples, and low-power techniques are discussed.

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Gary K. Yeap is the author of Practical Low Power Digital VLSI Design (4.17 avg rating, 6 ratings, 0 reviews, published 1997)

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